

# eDAS Block Diagram - Figure 1

Shaded blocks are all handled in software within the microprocessor

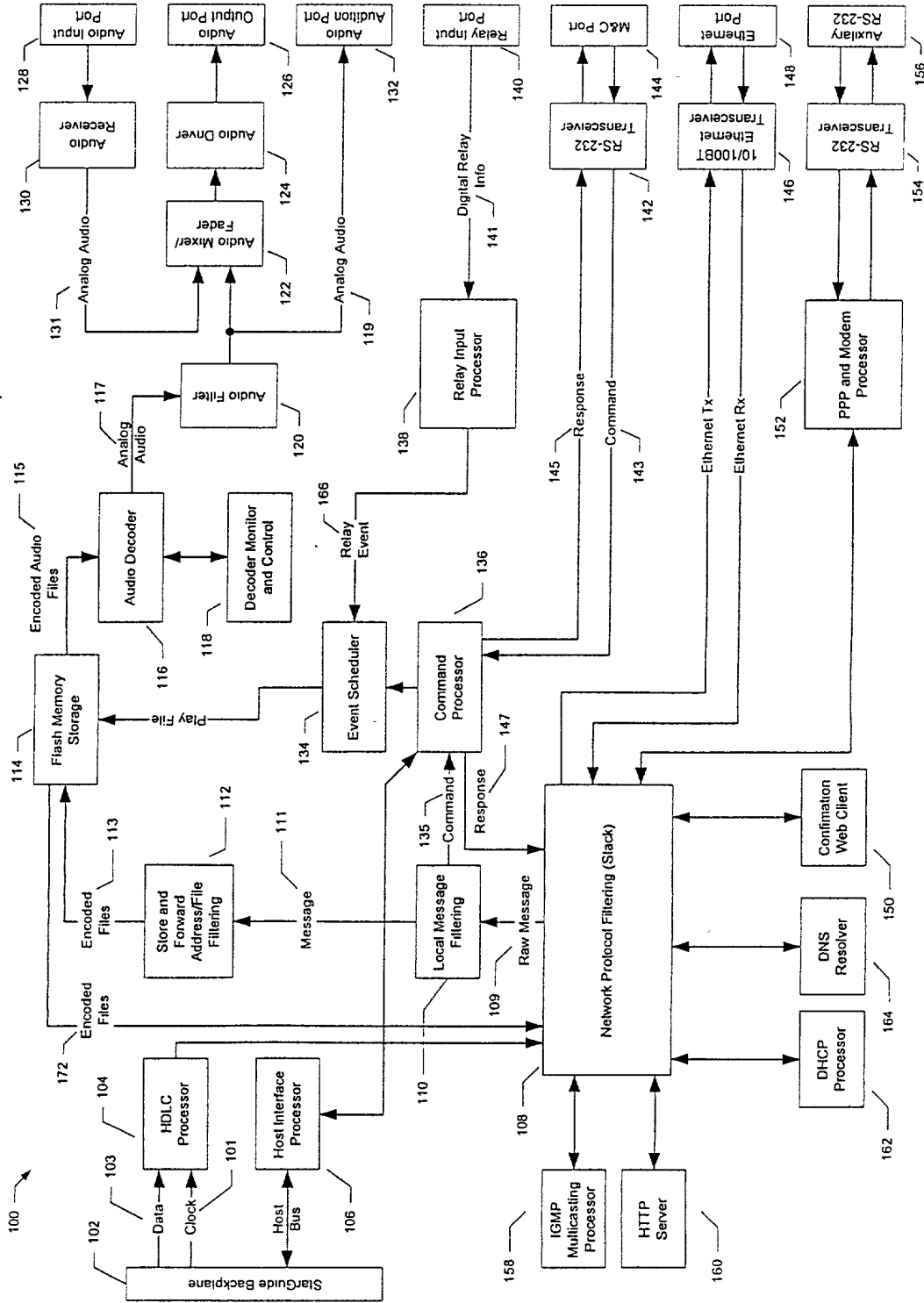
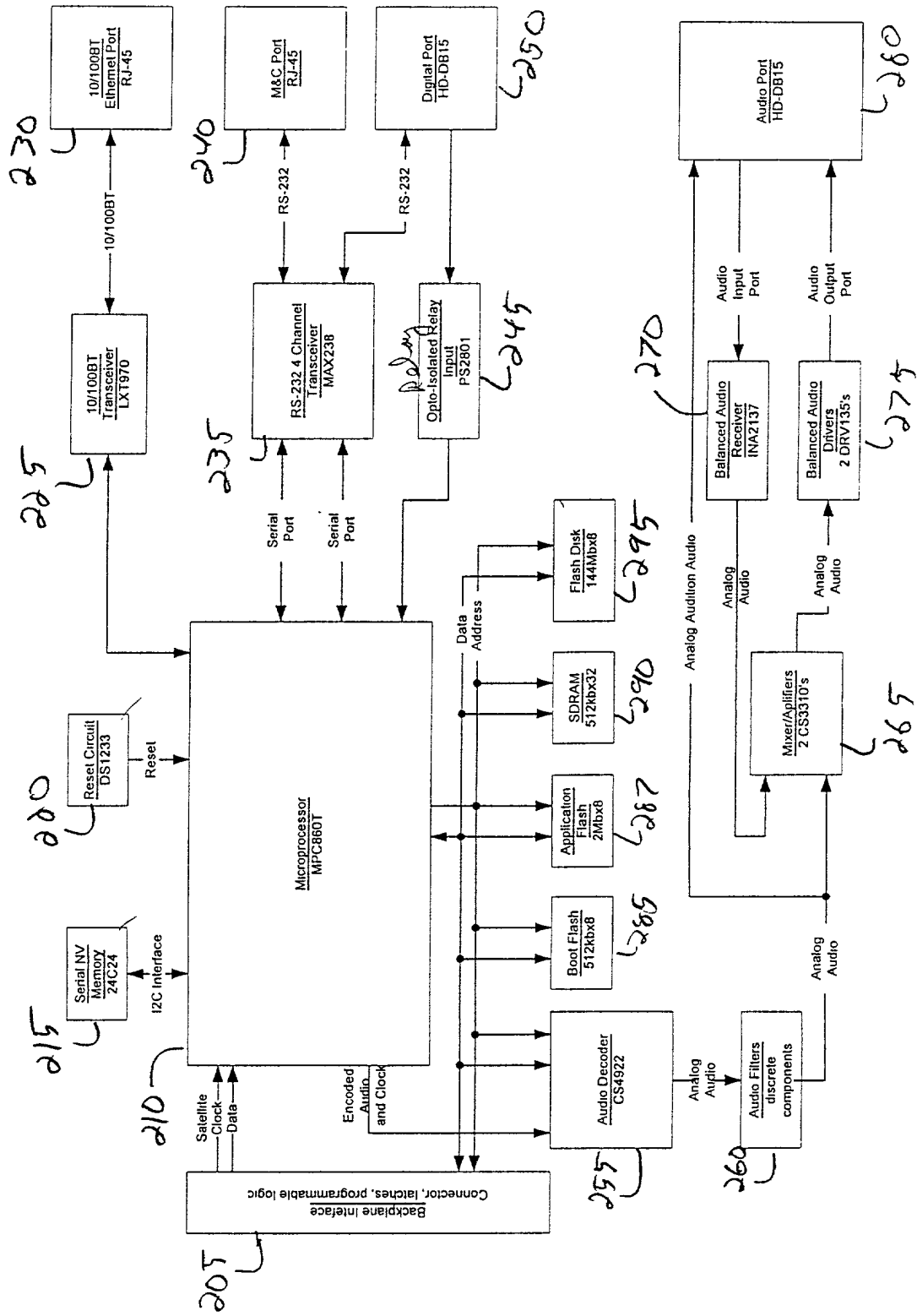
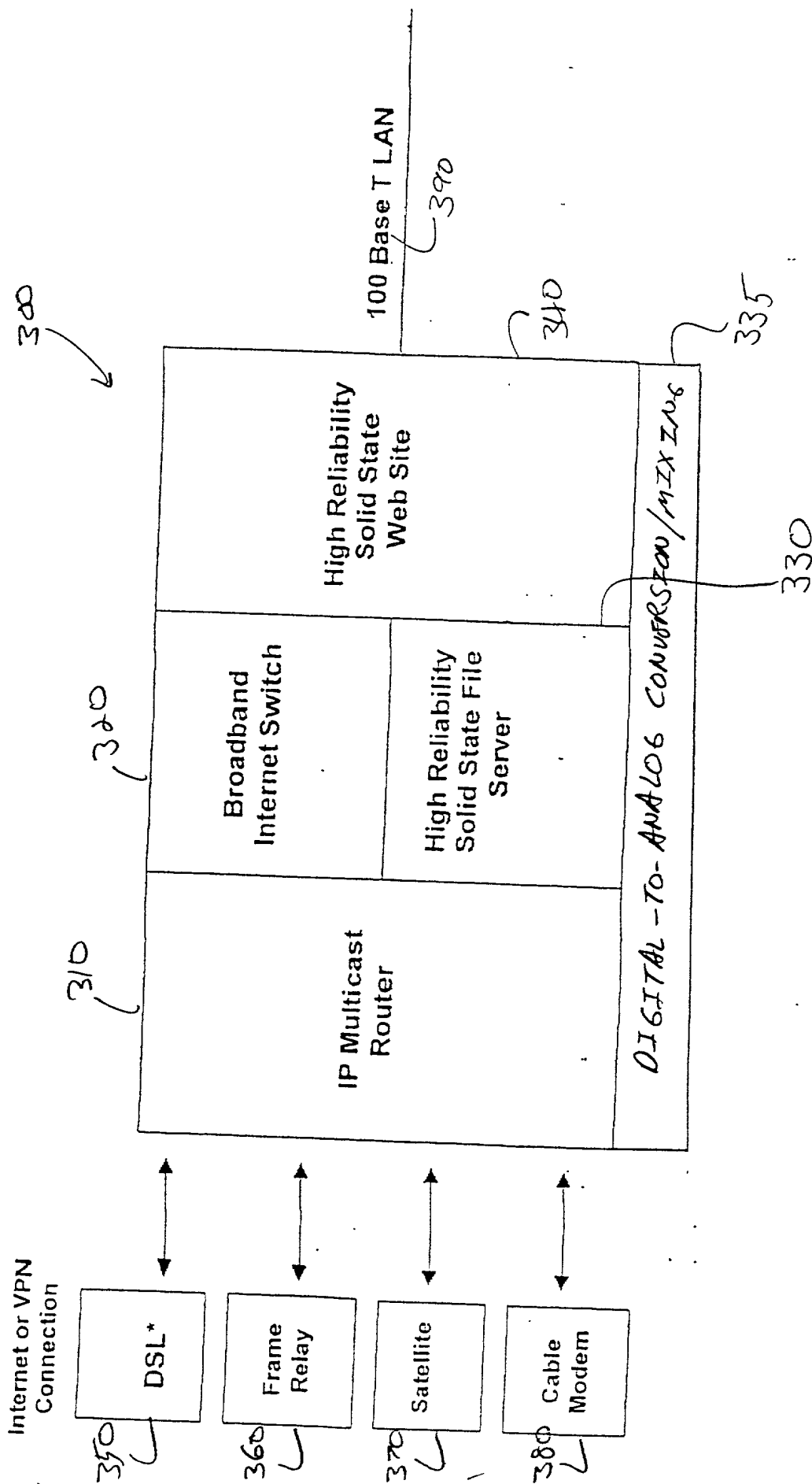


Figure 2. Hardware Block Diagram



StarGuide StarGate



000074 16/06/00

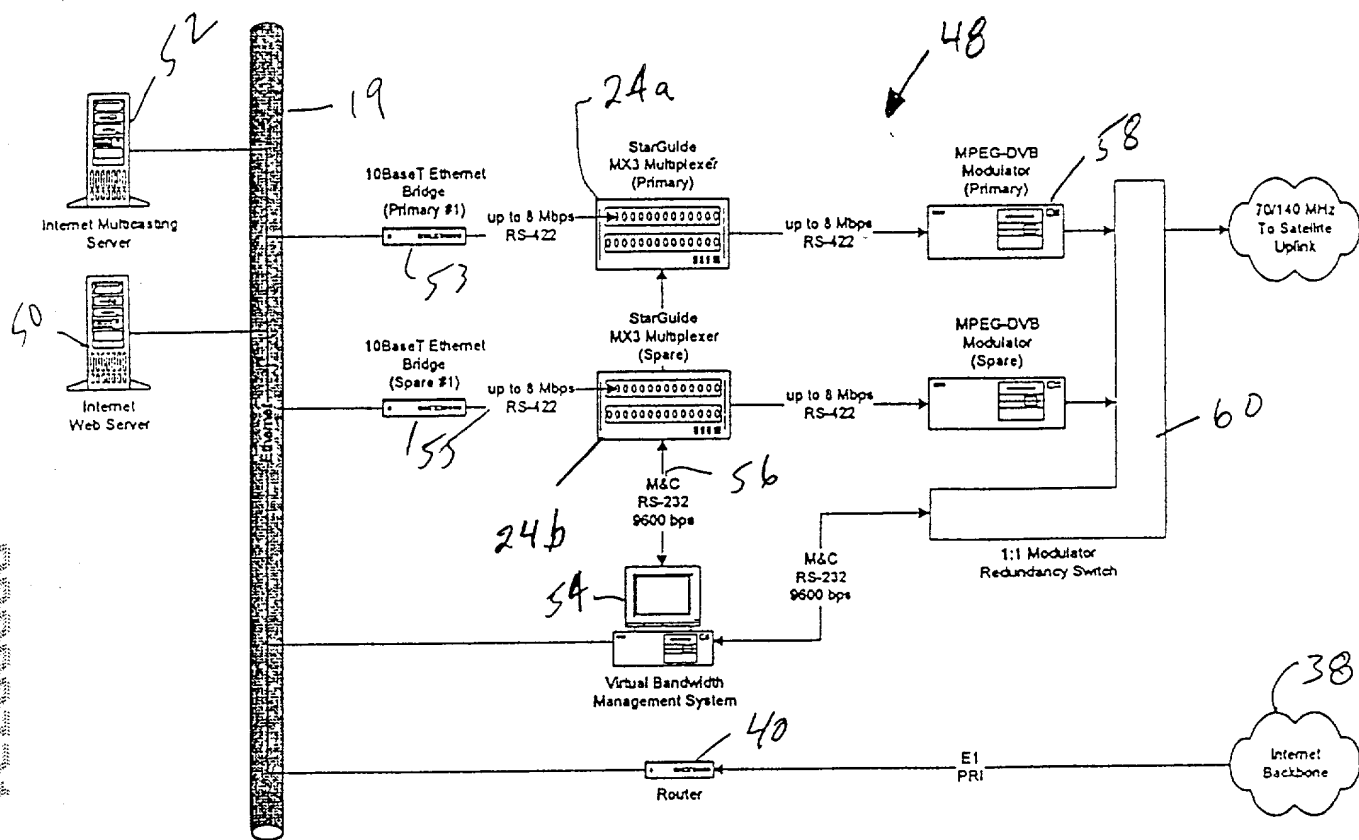


Fig 4

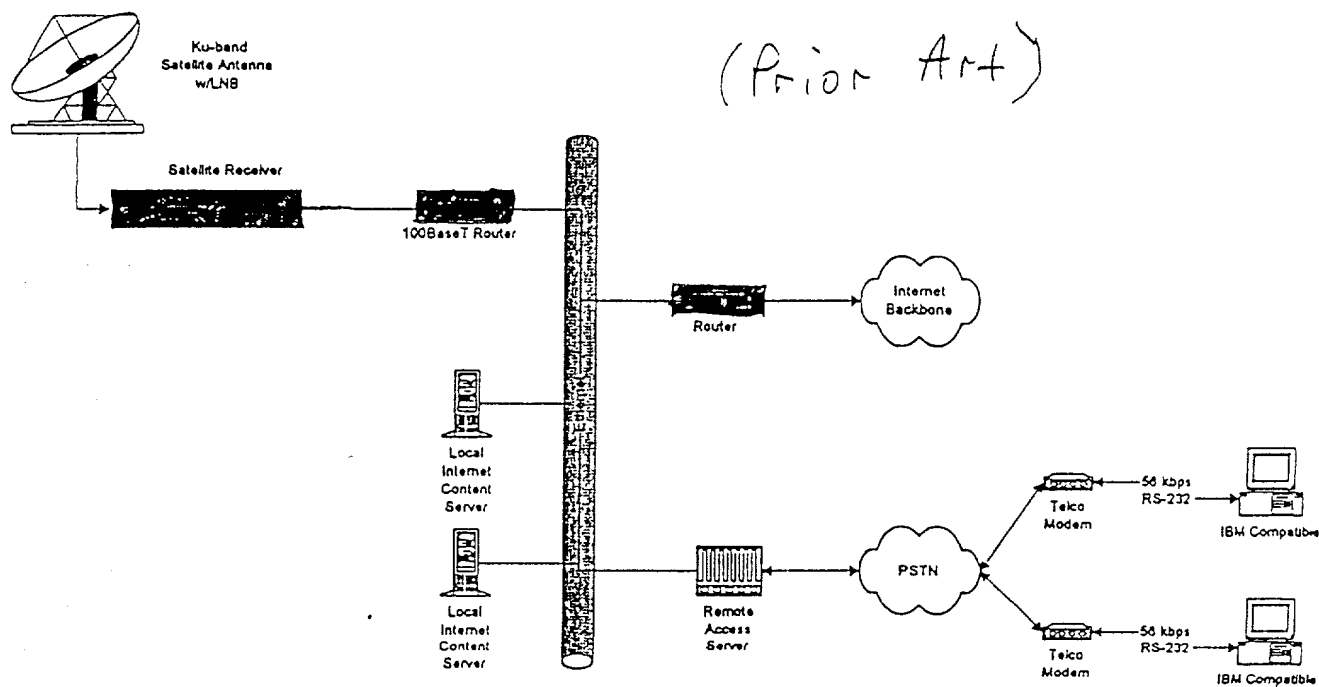


Fig 10

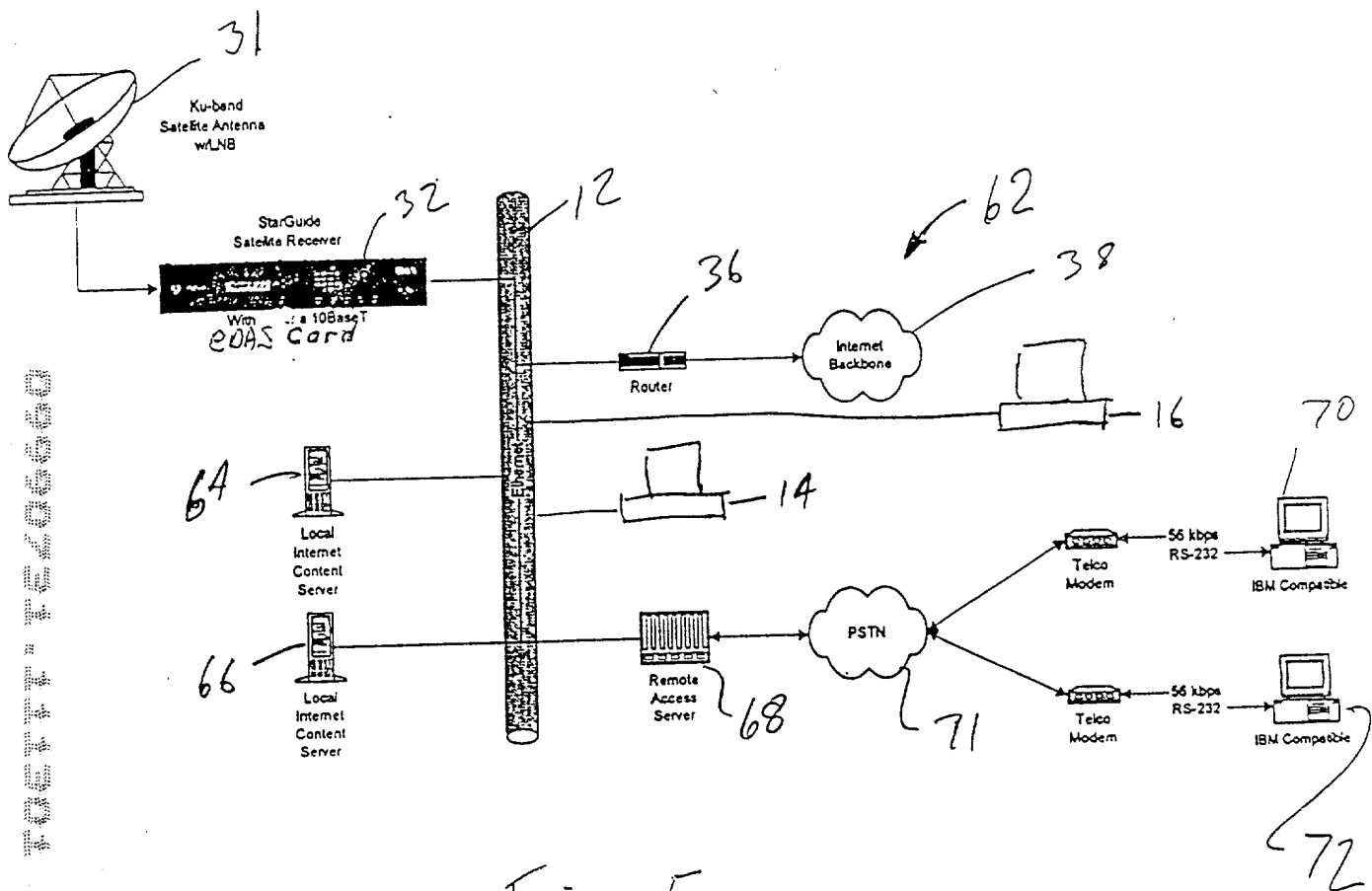


Fig. 5

00000734 11130

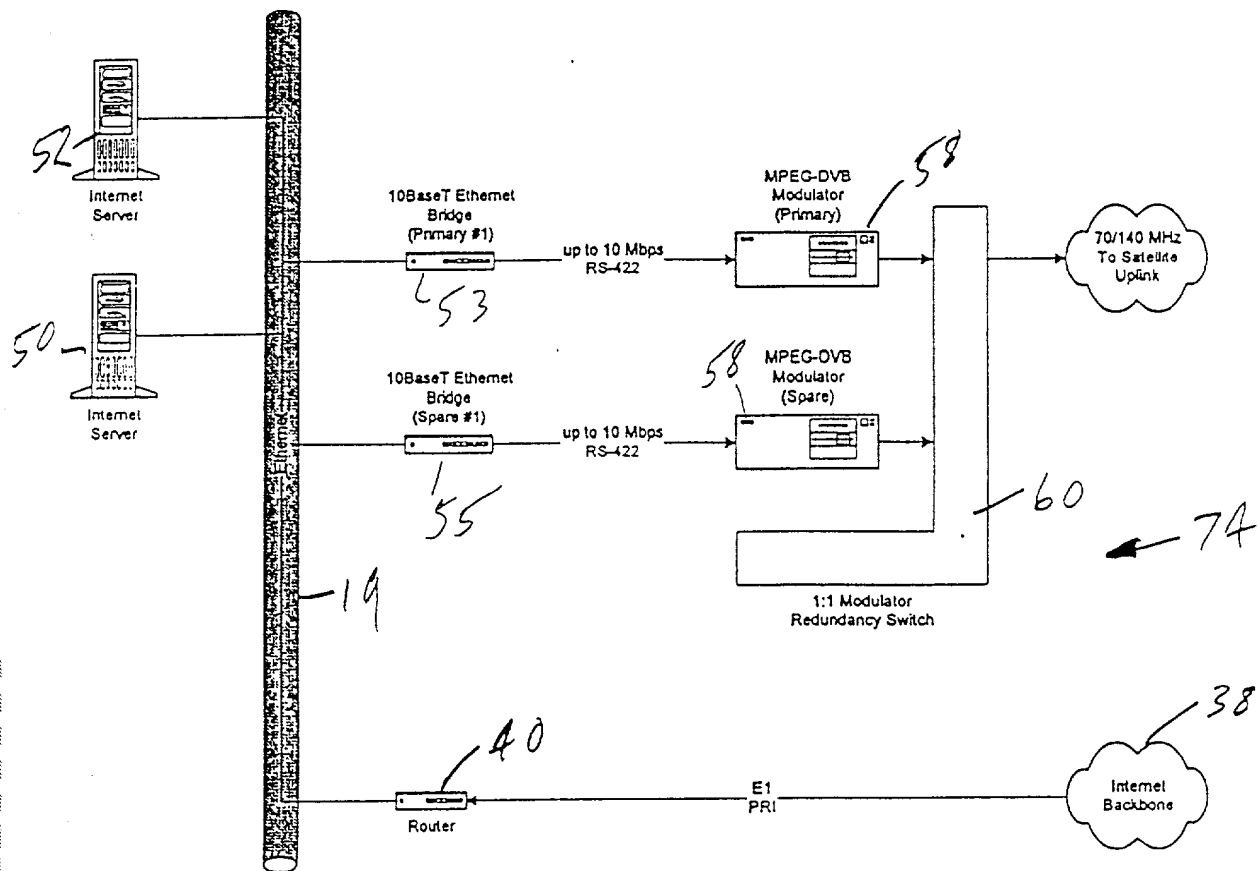


Fig. 6

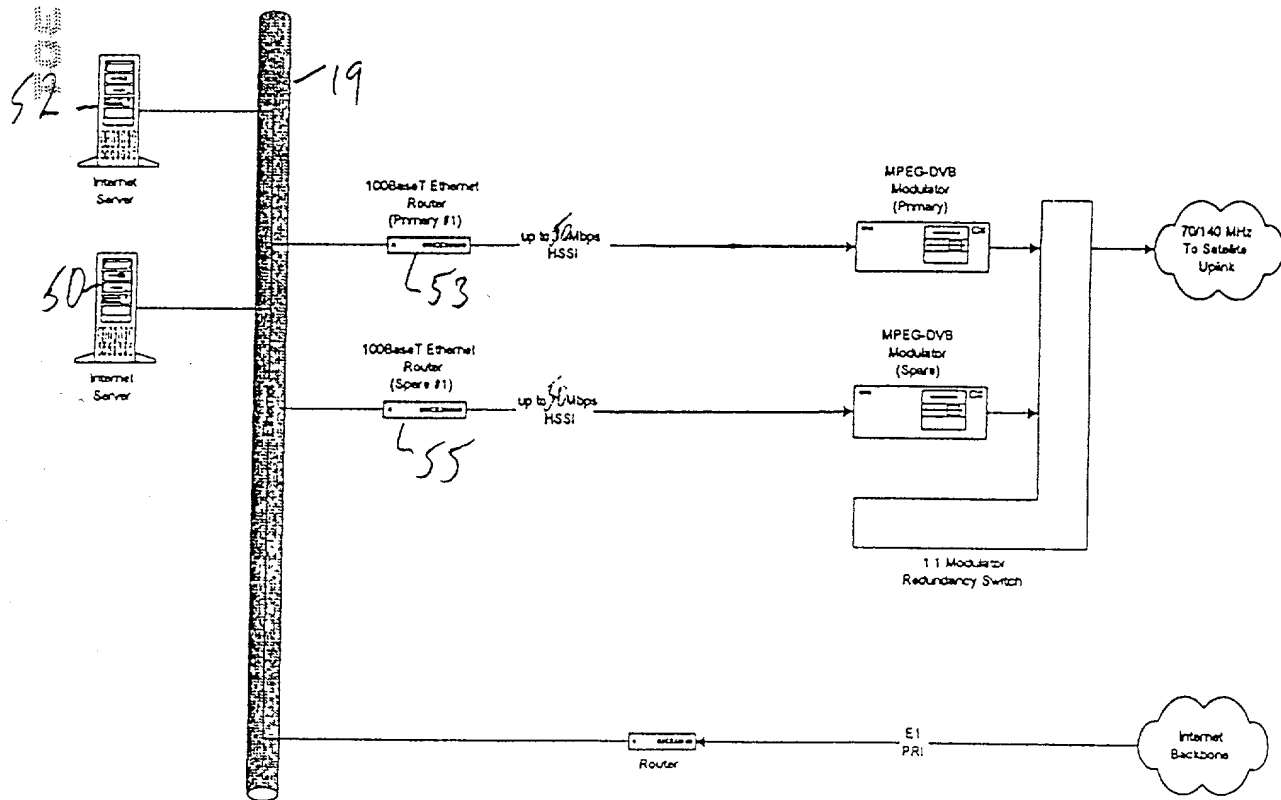
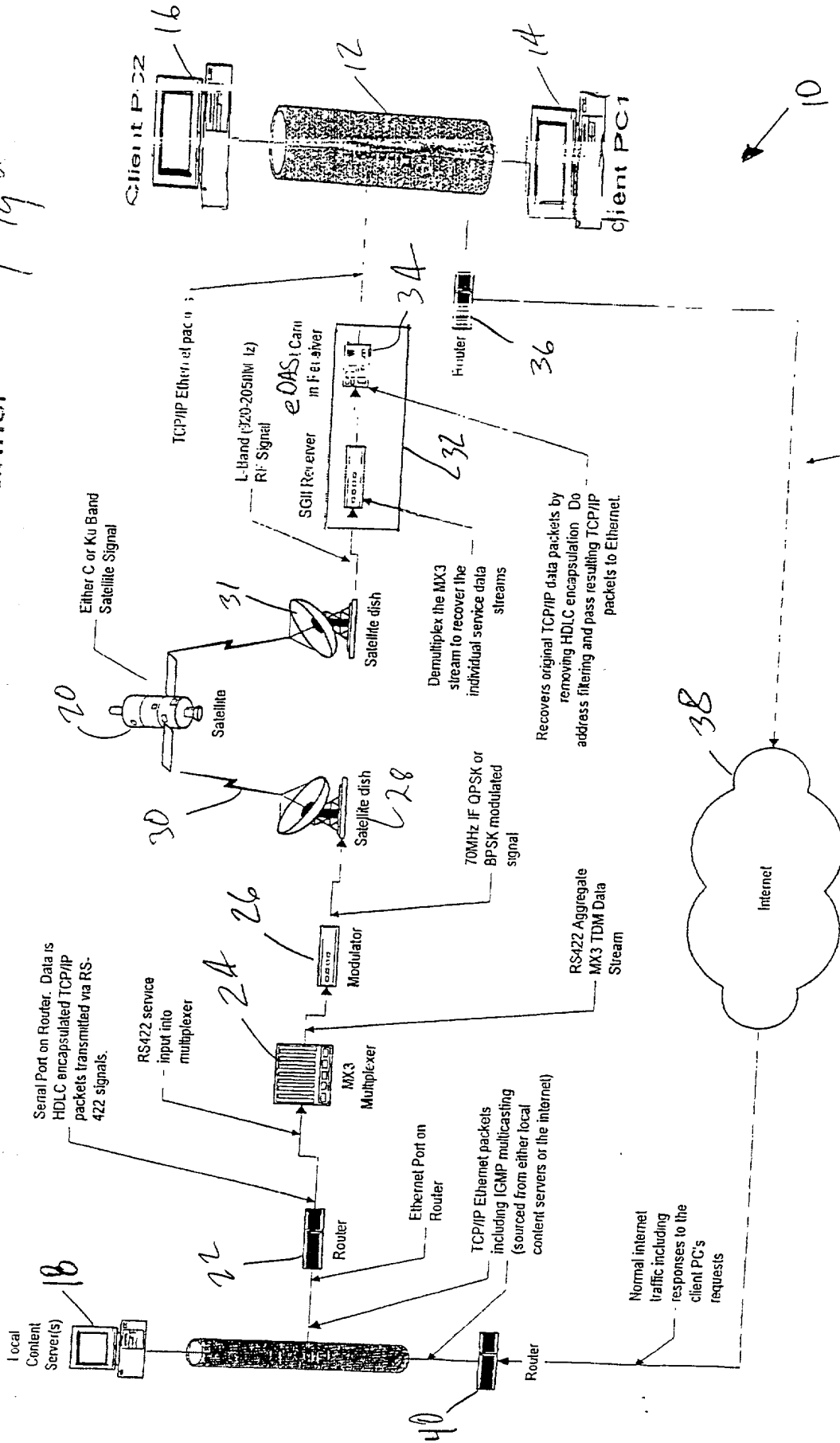


Fig. 7

# Asymmetric Internet System Data Flow Using Internet Backchannel

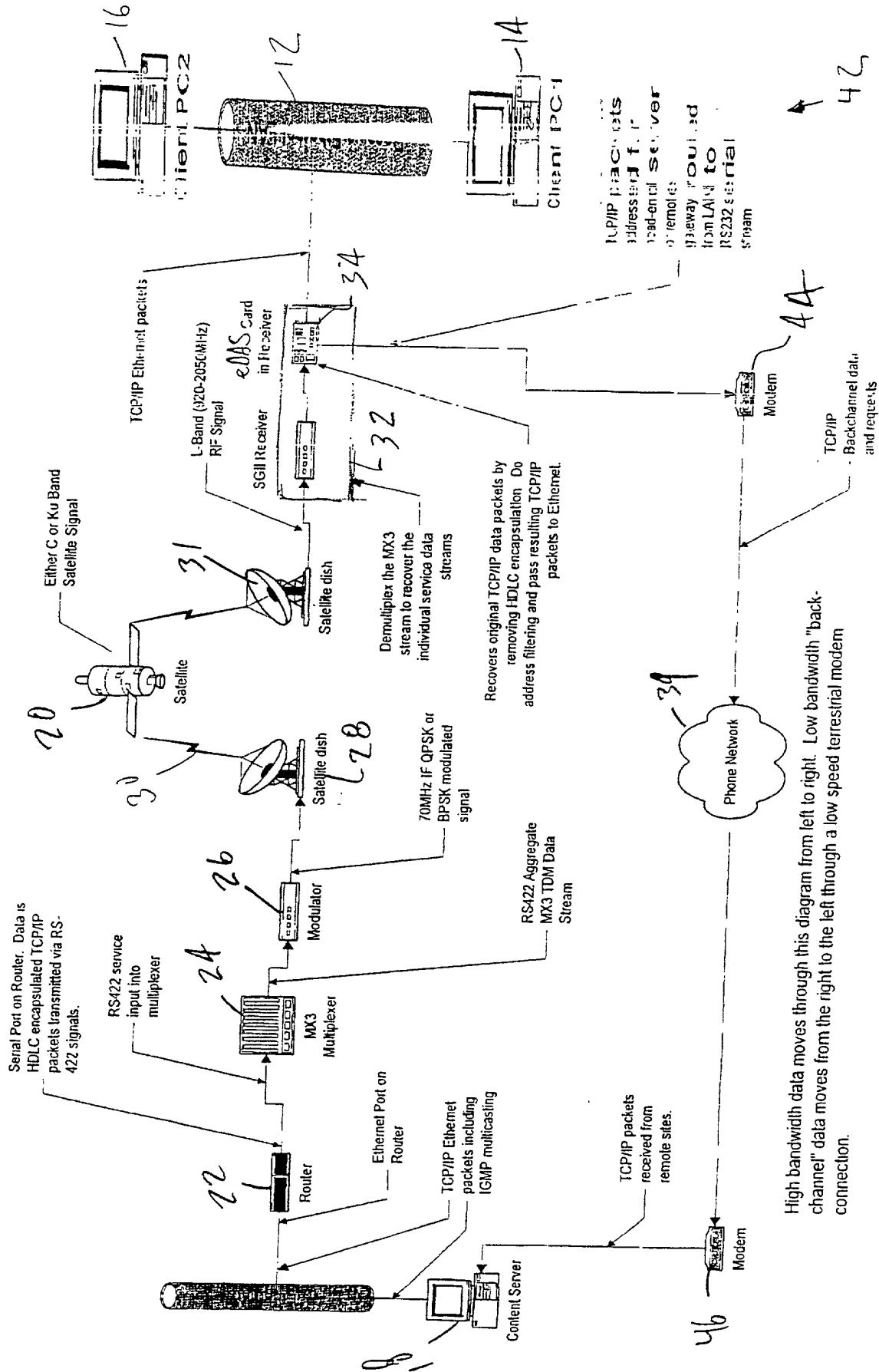
Figure 8



High bandwidth data moves through this diagram from left to right. Low bandwidth data moves from the right to the left through a less reliable internet connection. As client PC's request data, the responses are routed through the satellite head-end to the client through the Ethernet Card.

Requests for CP/RT the server IP address is mapped to the satellite head-end server point. The server head-end is the satellite head-end and are routed back to the client PC through the Ethernet Card.

Figure 9 Closed Loop Network (Intranet) Capable of Delivering TCP/IP Data Using External Telco Modem Backchannel



For TCP/IP data, requests for data are generated by the client PC's (for example, Client PC1), routed via the Ethernet Card through the terrestrial backchannel. The data is routed from the content server over the satellite channel (high speed) and routed by the Ethernet Card to the appropriate client PC



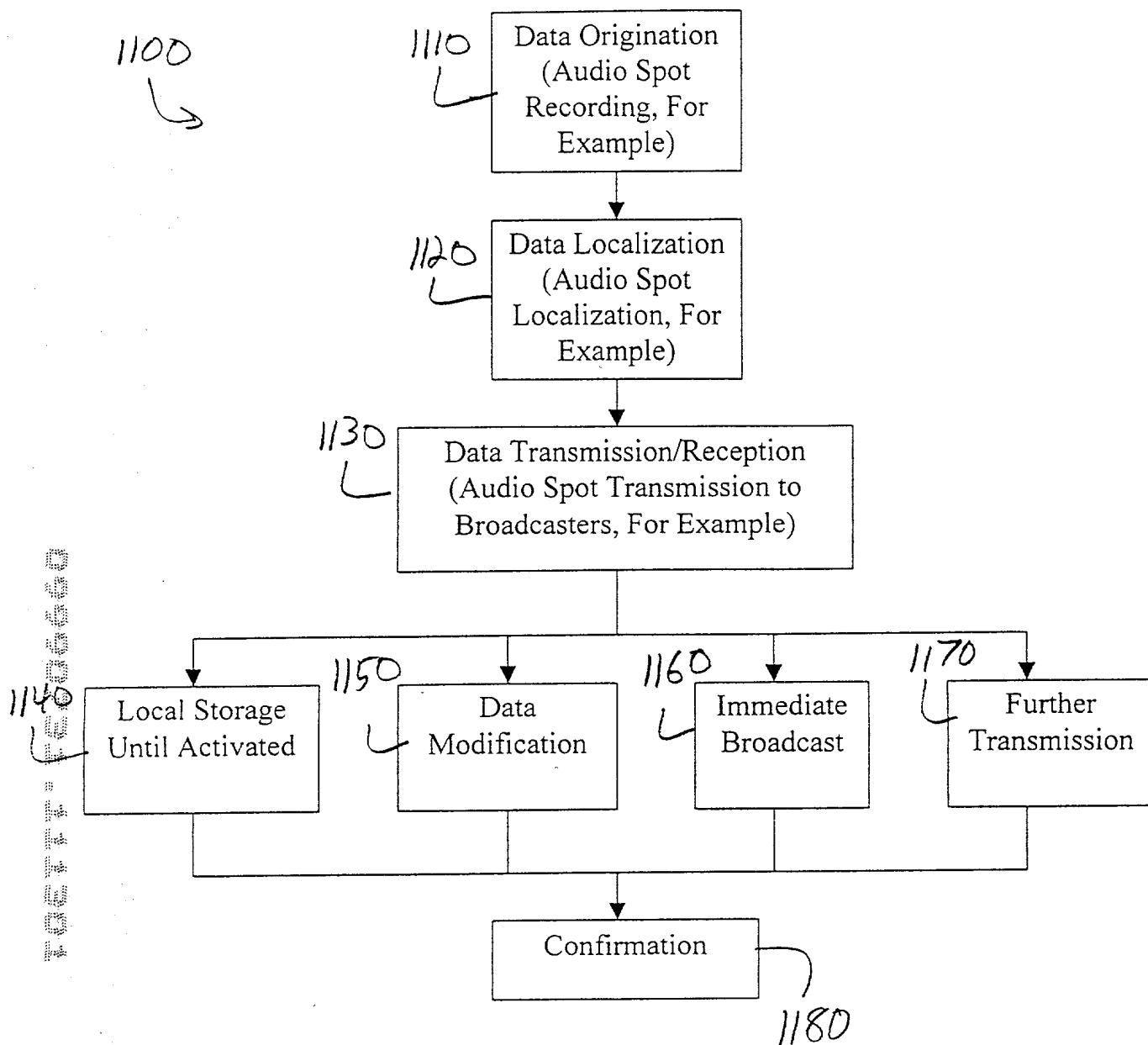


Figure 11

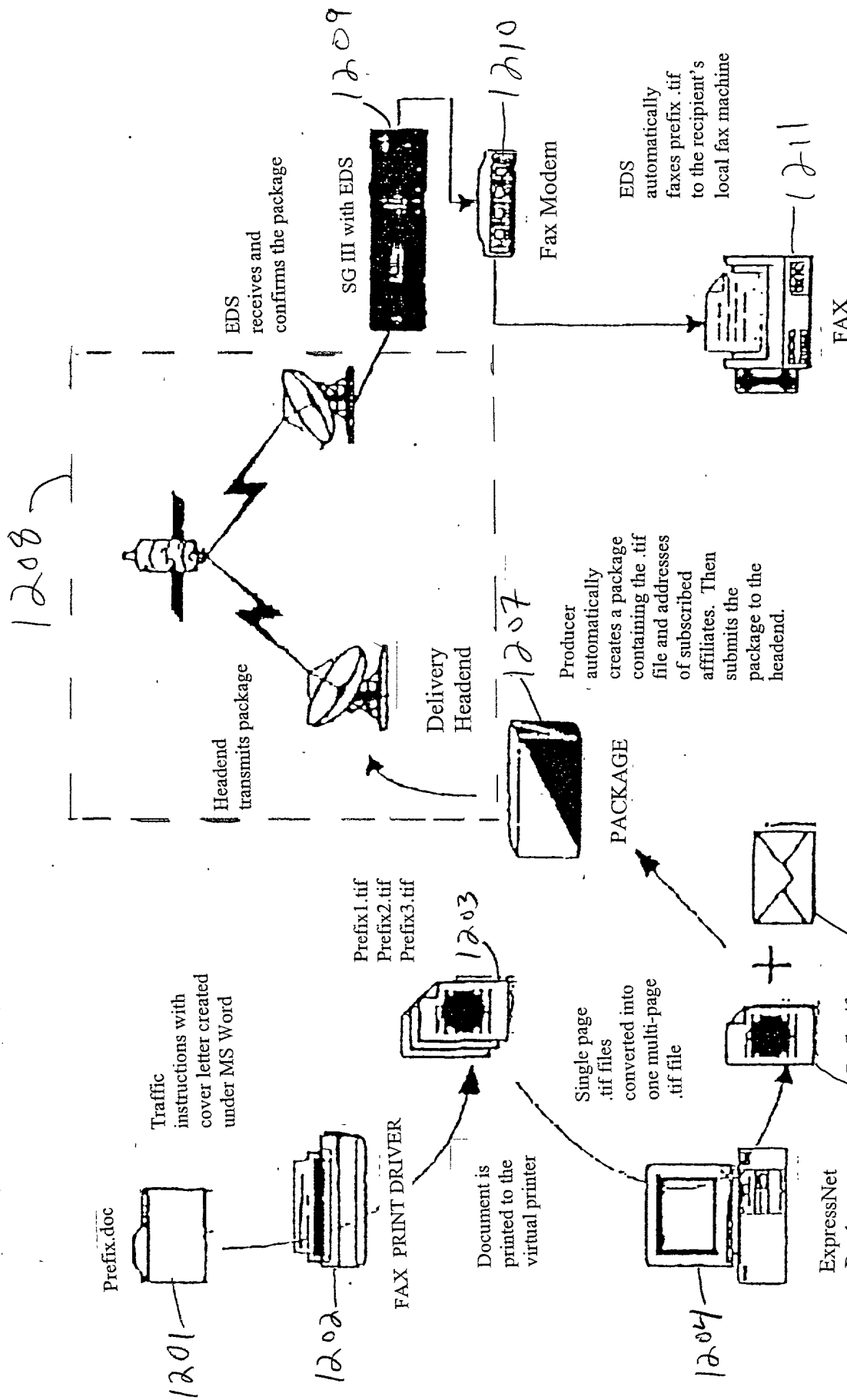


FIG. 12

FIG. 6

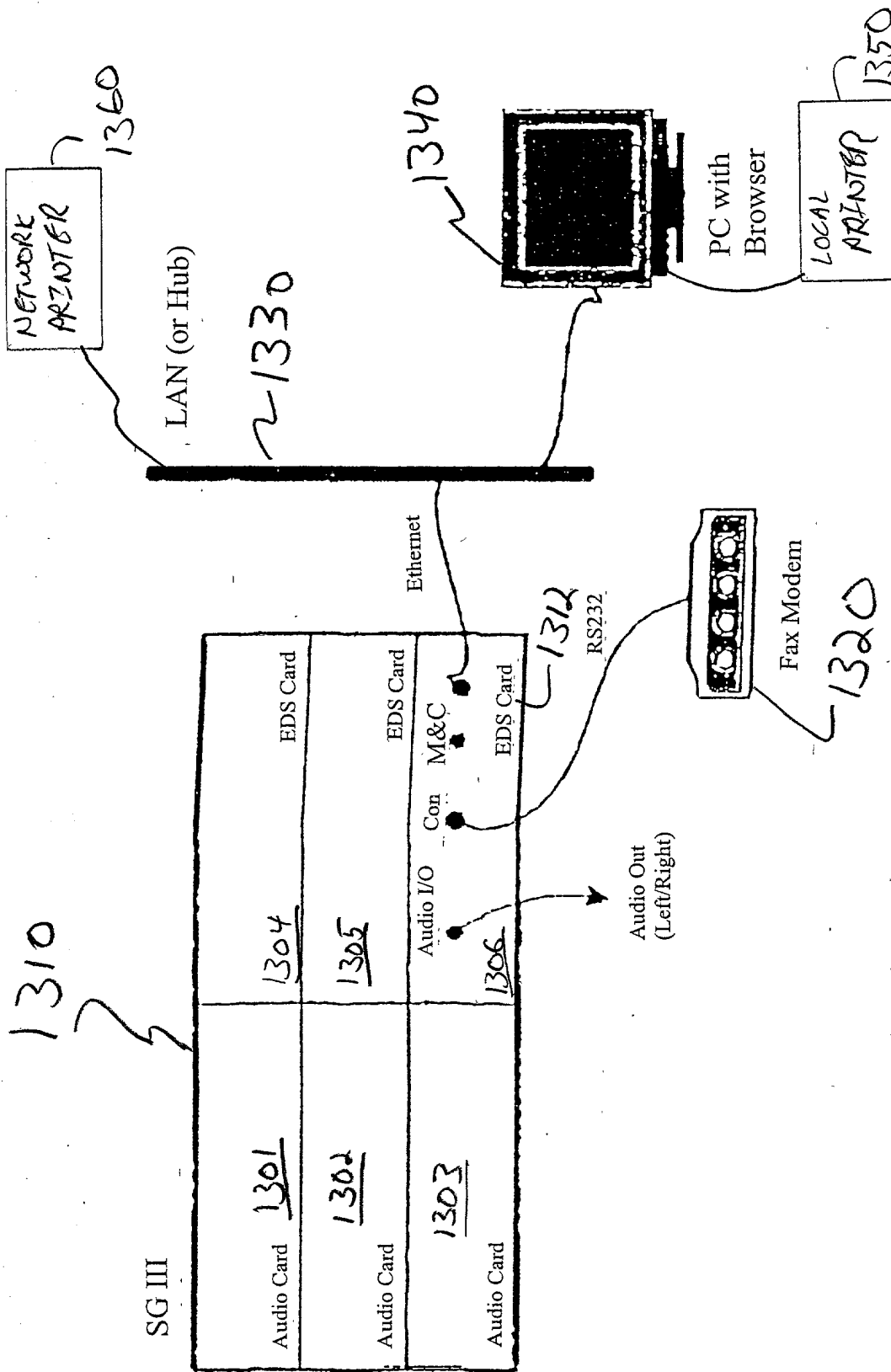


FIG. 6.13